

FIG. 1

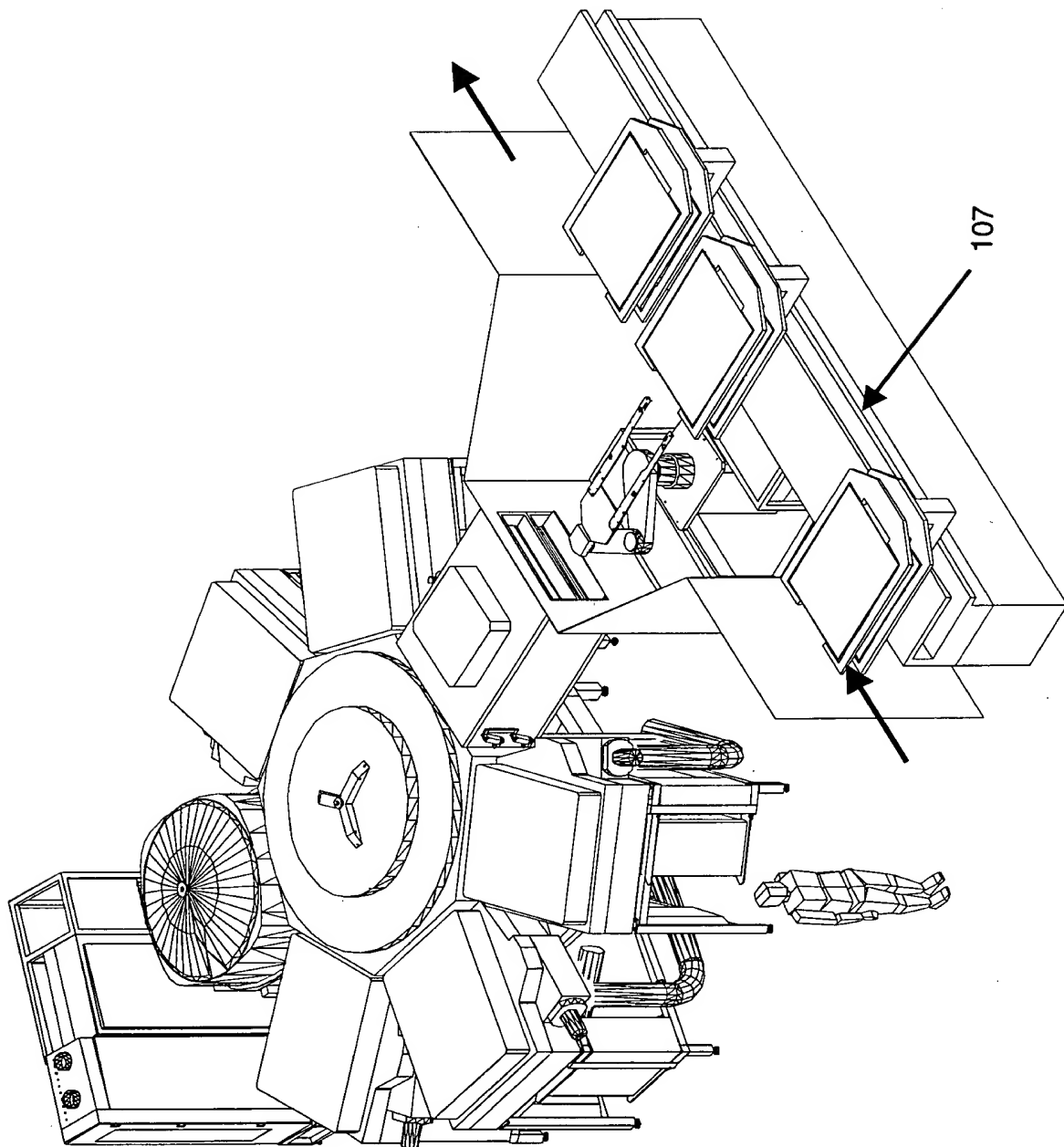


FIG. 2

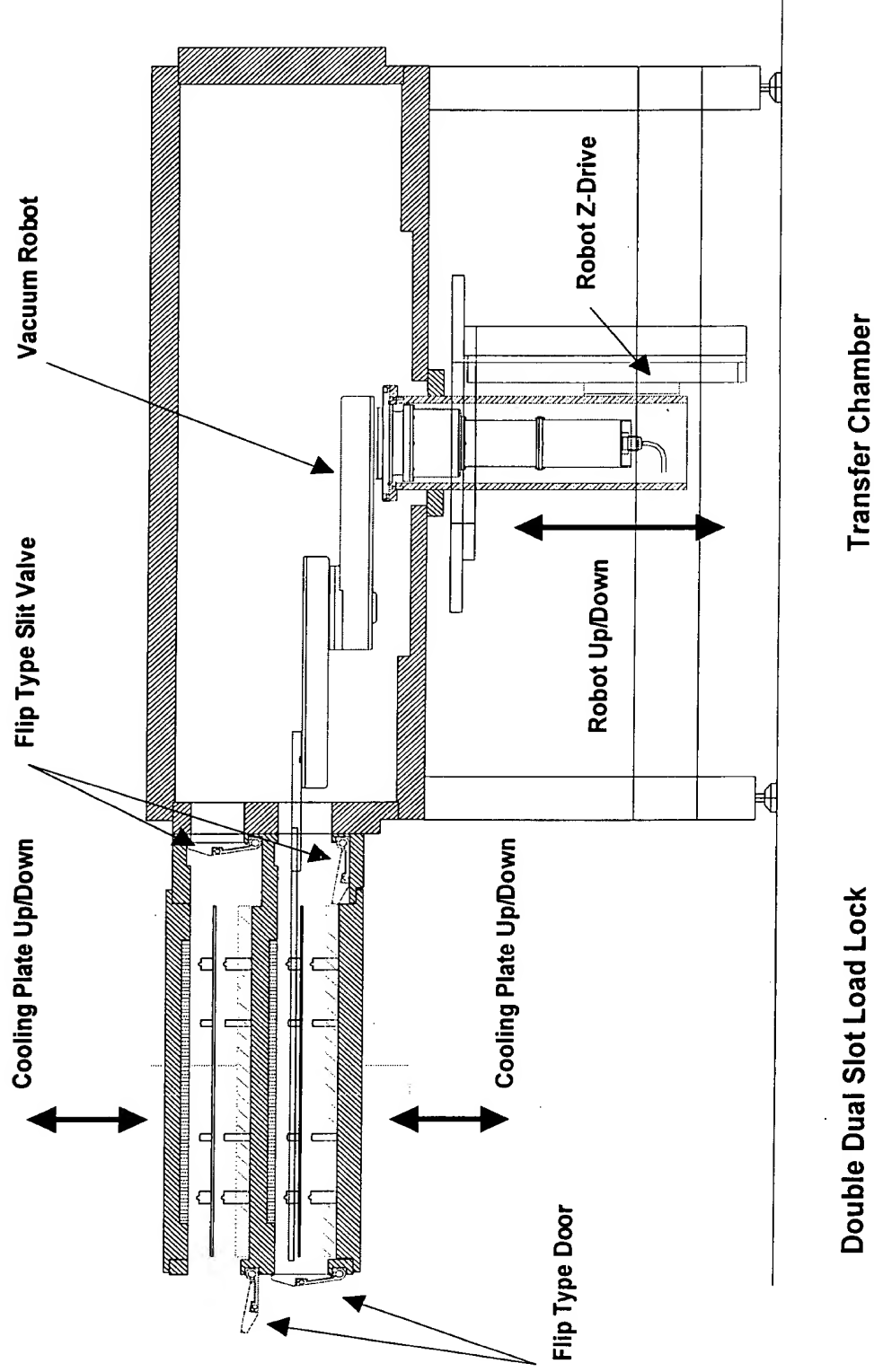
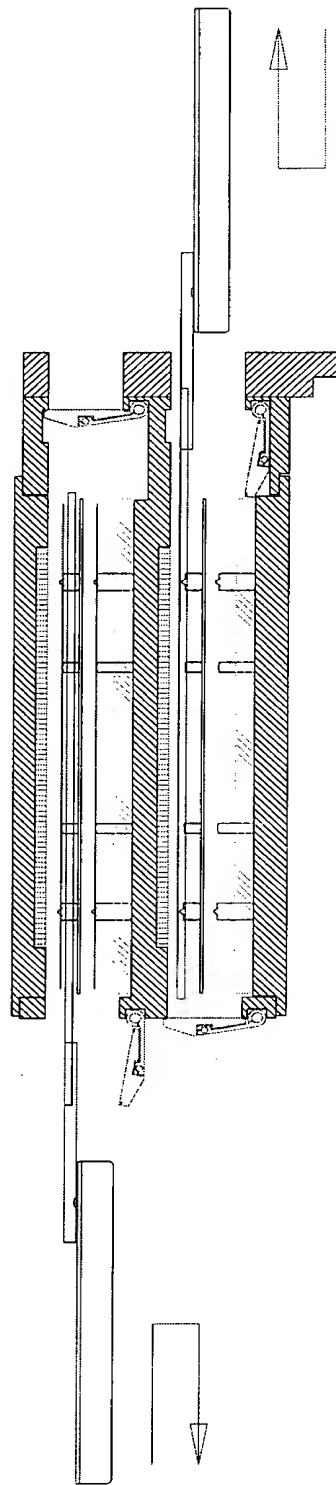


FIG. 3



ACLS Side
Load a Pre-processed Substrate

DDSL

Transfer Chamber Side
Unload a Pre-processed Substrate

FIG. 4

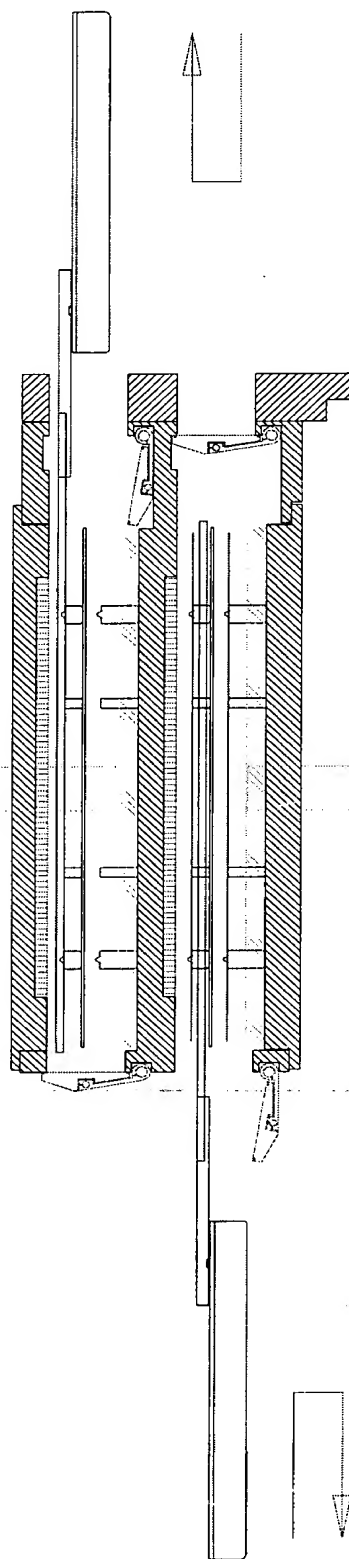
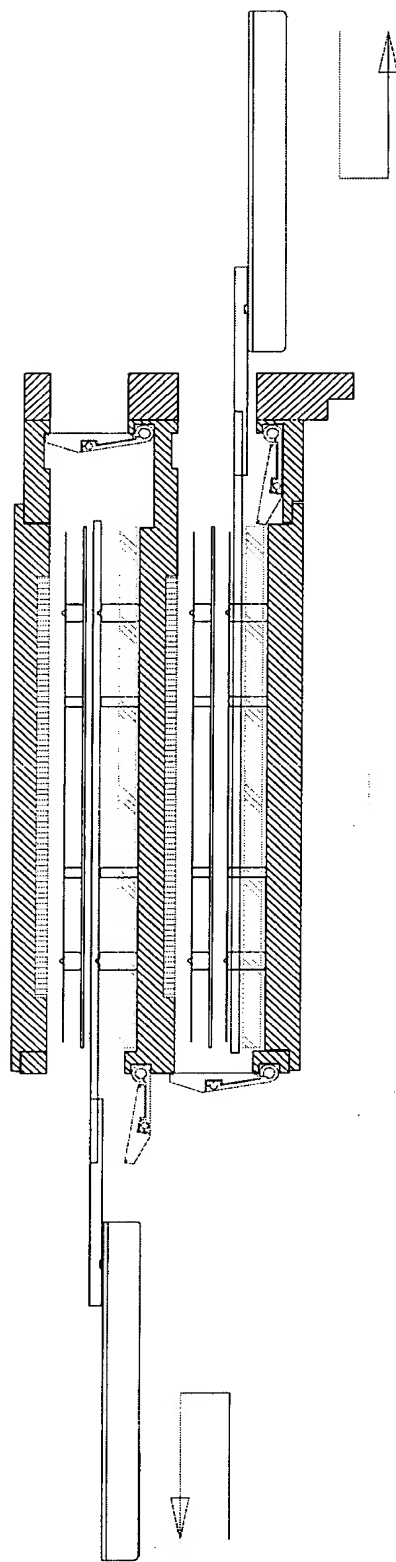


FIG. 5



ACLS Side
Unload a Processed Substrate

DDSL

Transfer Chamber Side
Load a Processed Substrate

FIG. 6

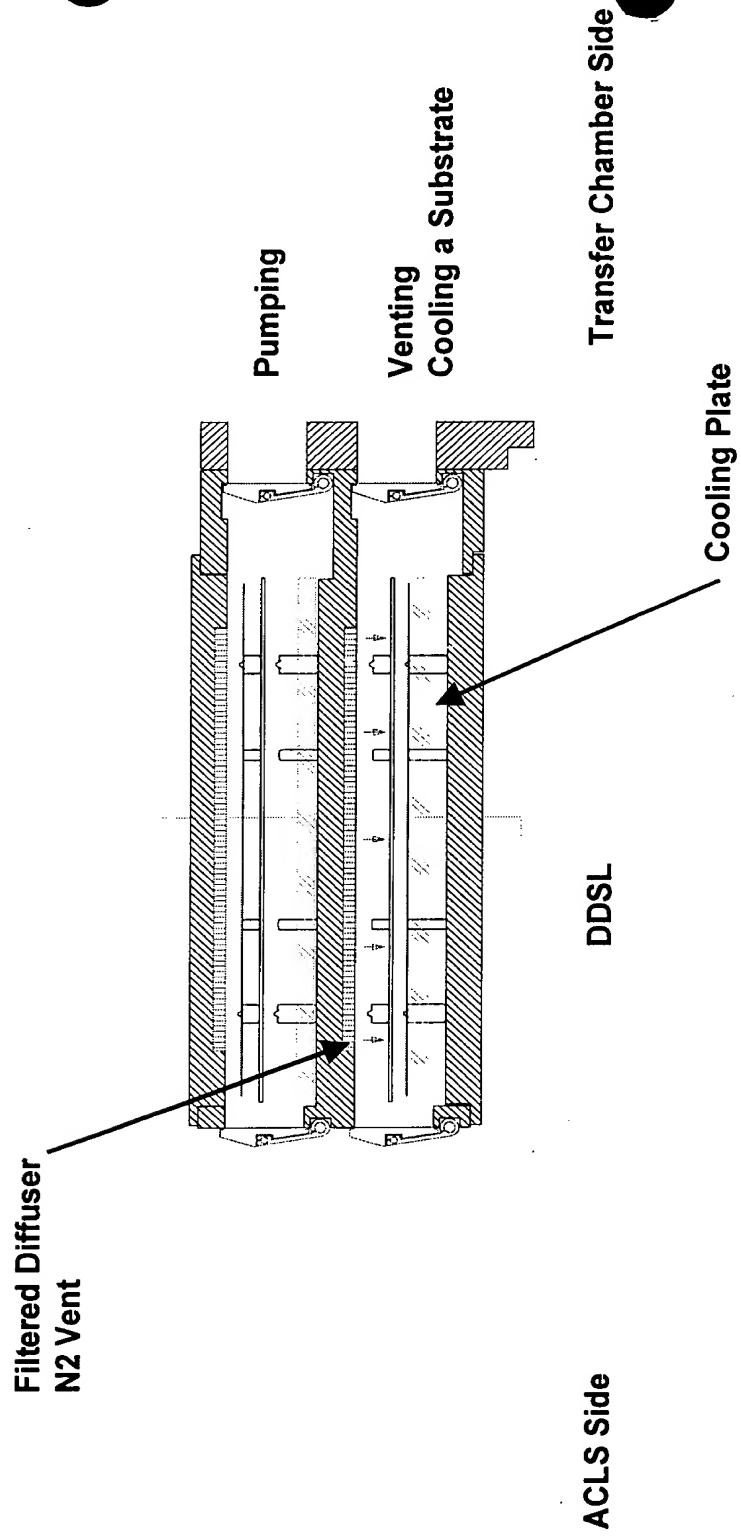


FIG. 7

